

Docket No./Serial No. (if known): 57321US002
Inventor(s): Paul F. Baude et al.
Title: IN-LINE DEPOSITION PROCESSES FOR CIRCUIT
FABRRICATION

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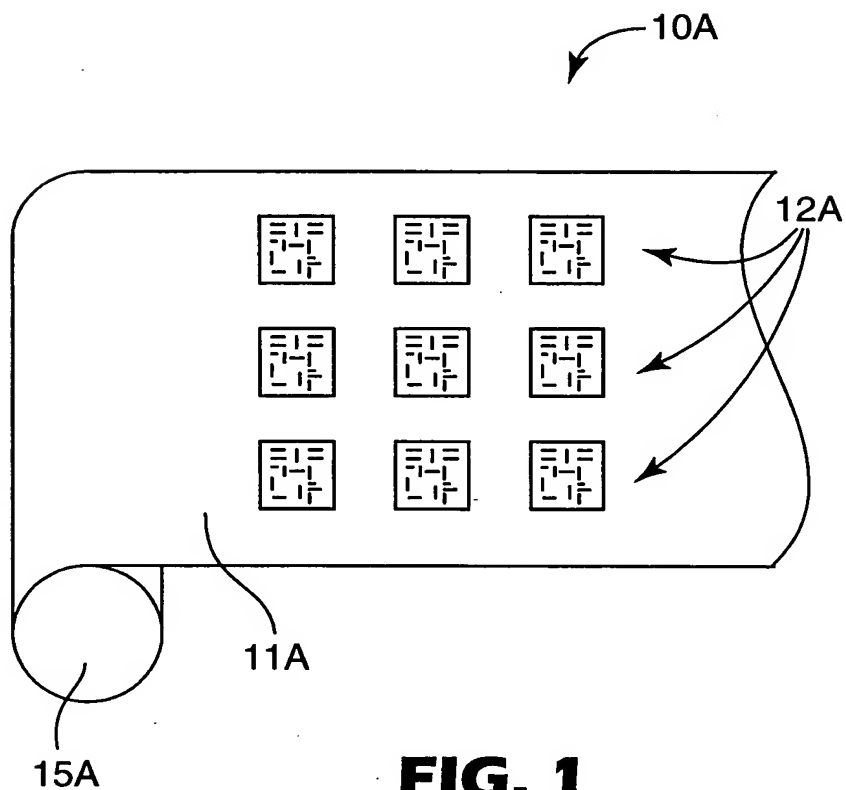


FIG. 1

10076005 "021402"

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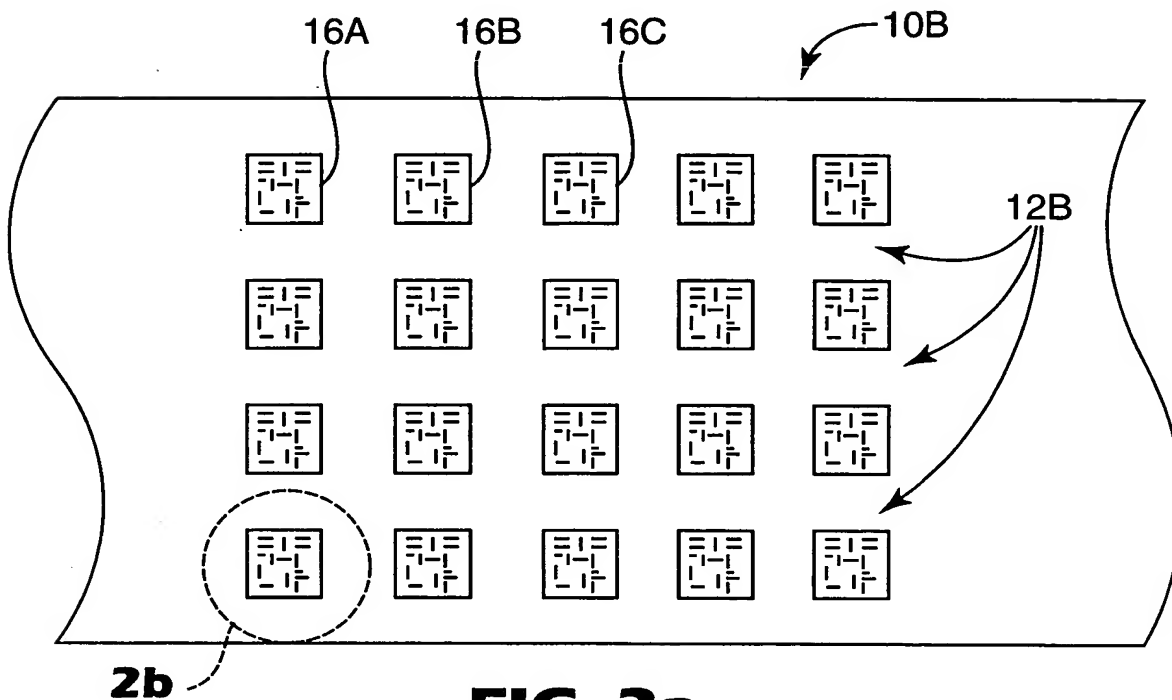


FIG. 2a

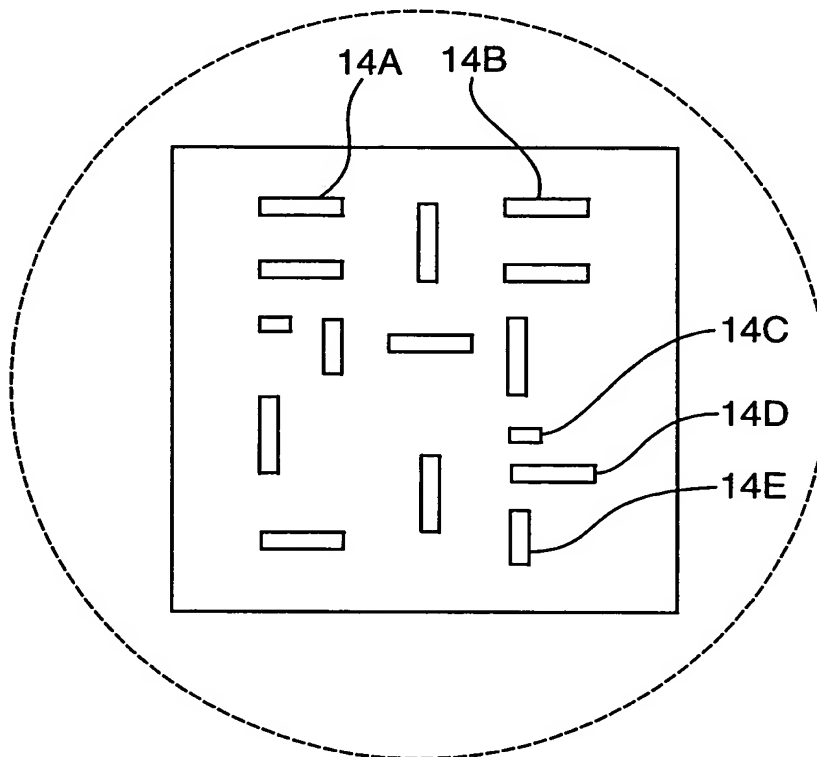


FIG. 2b

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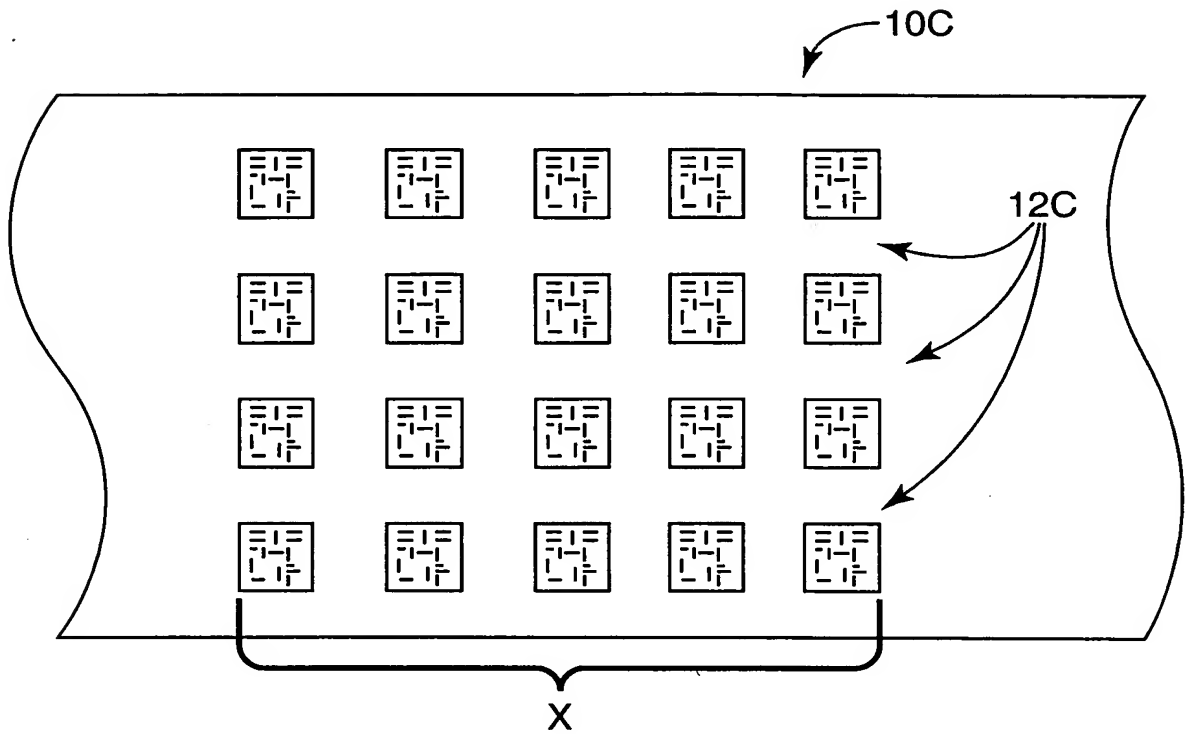


FIG. 3

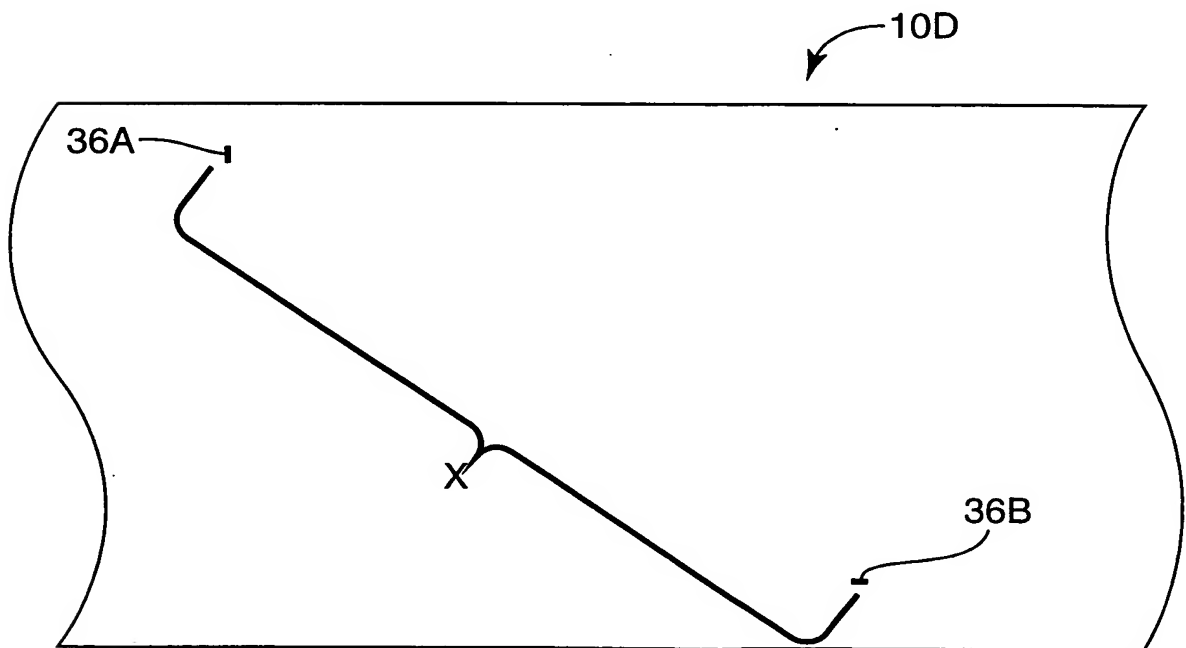


FIG. 4

2044220 50097001

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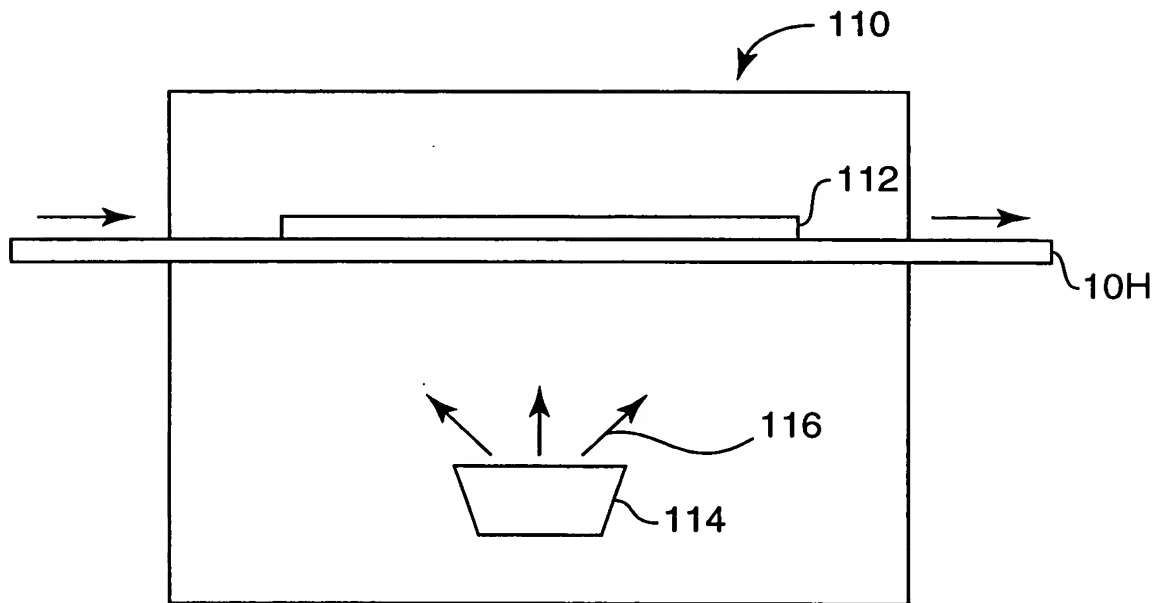


FIG. 10

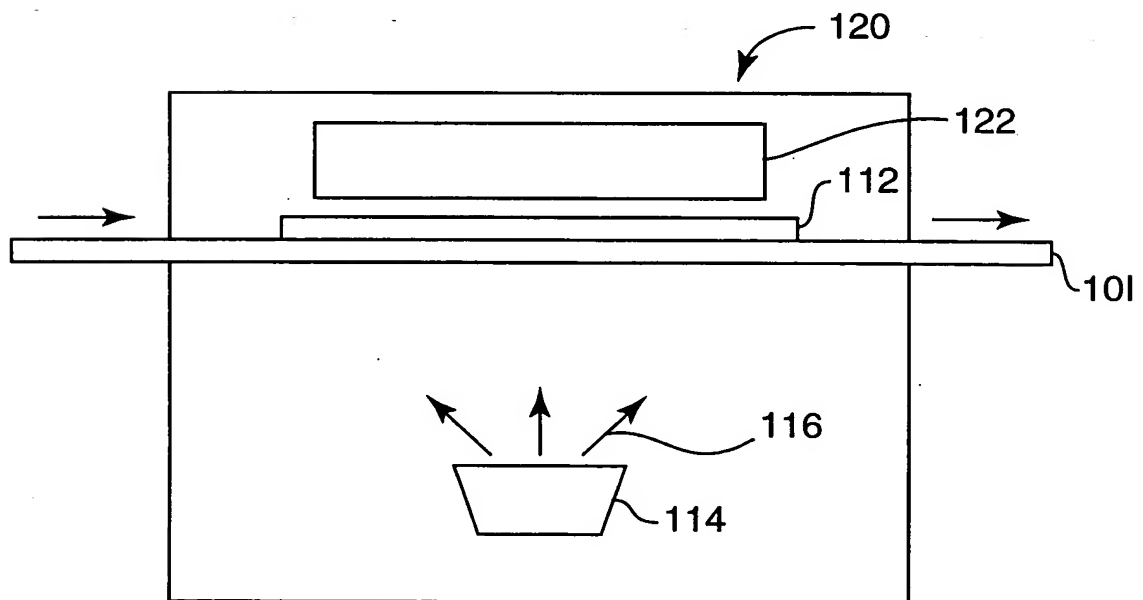


FIG. 11

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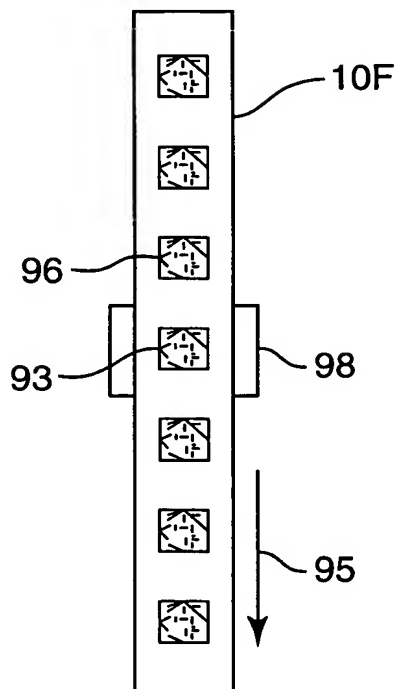


FIG. 8

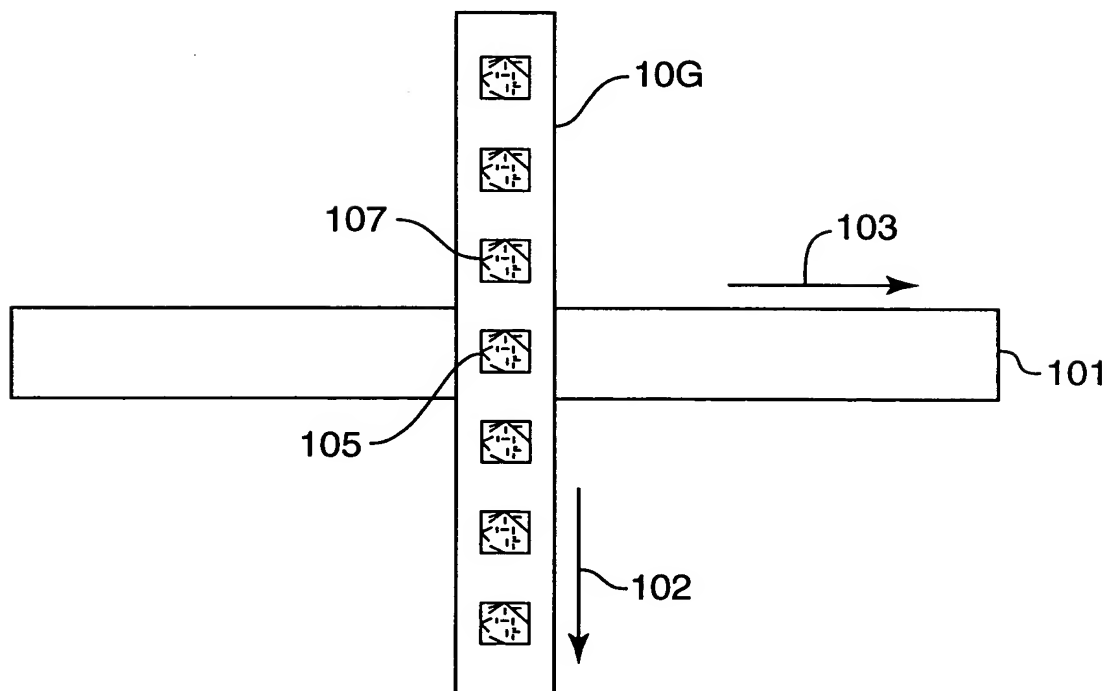


FIG. 9

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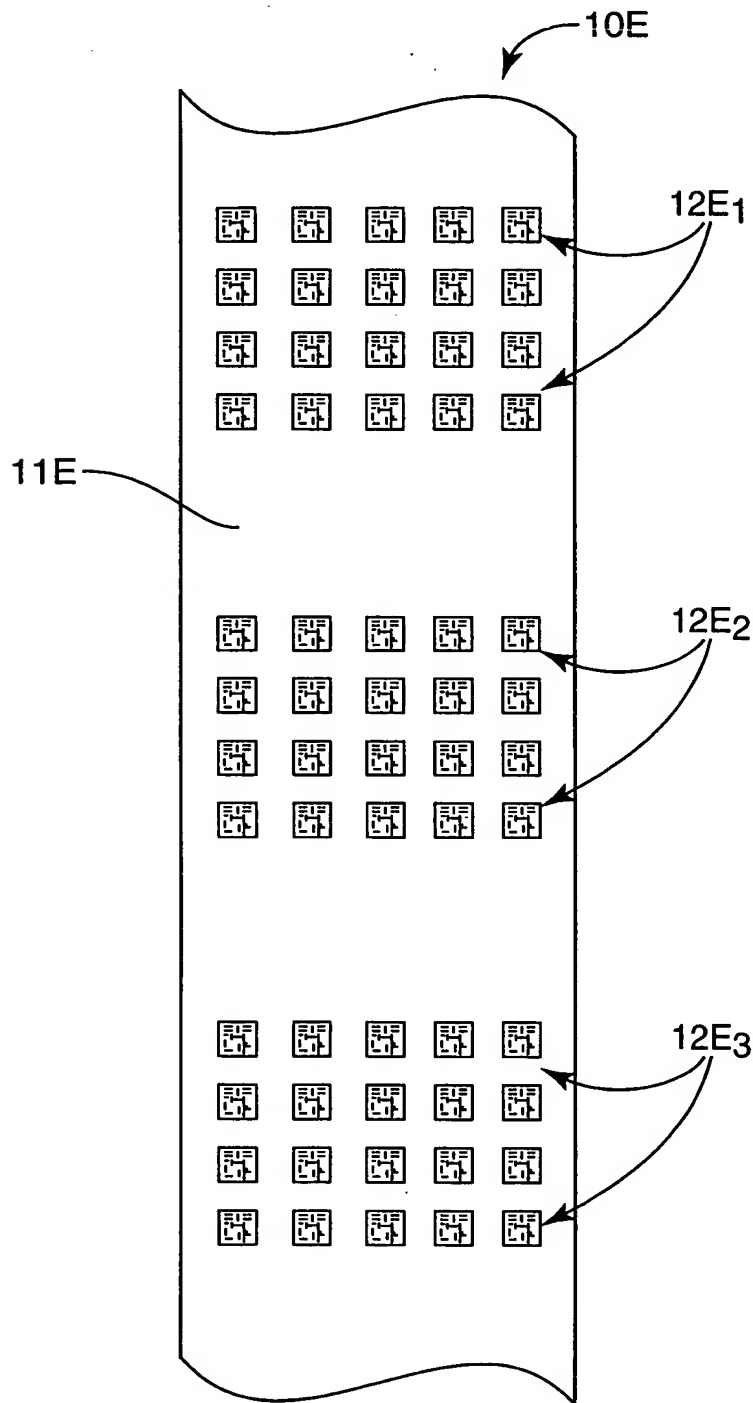


FIG. 5

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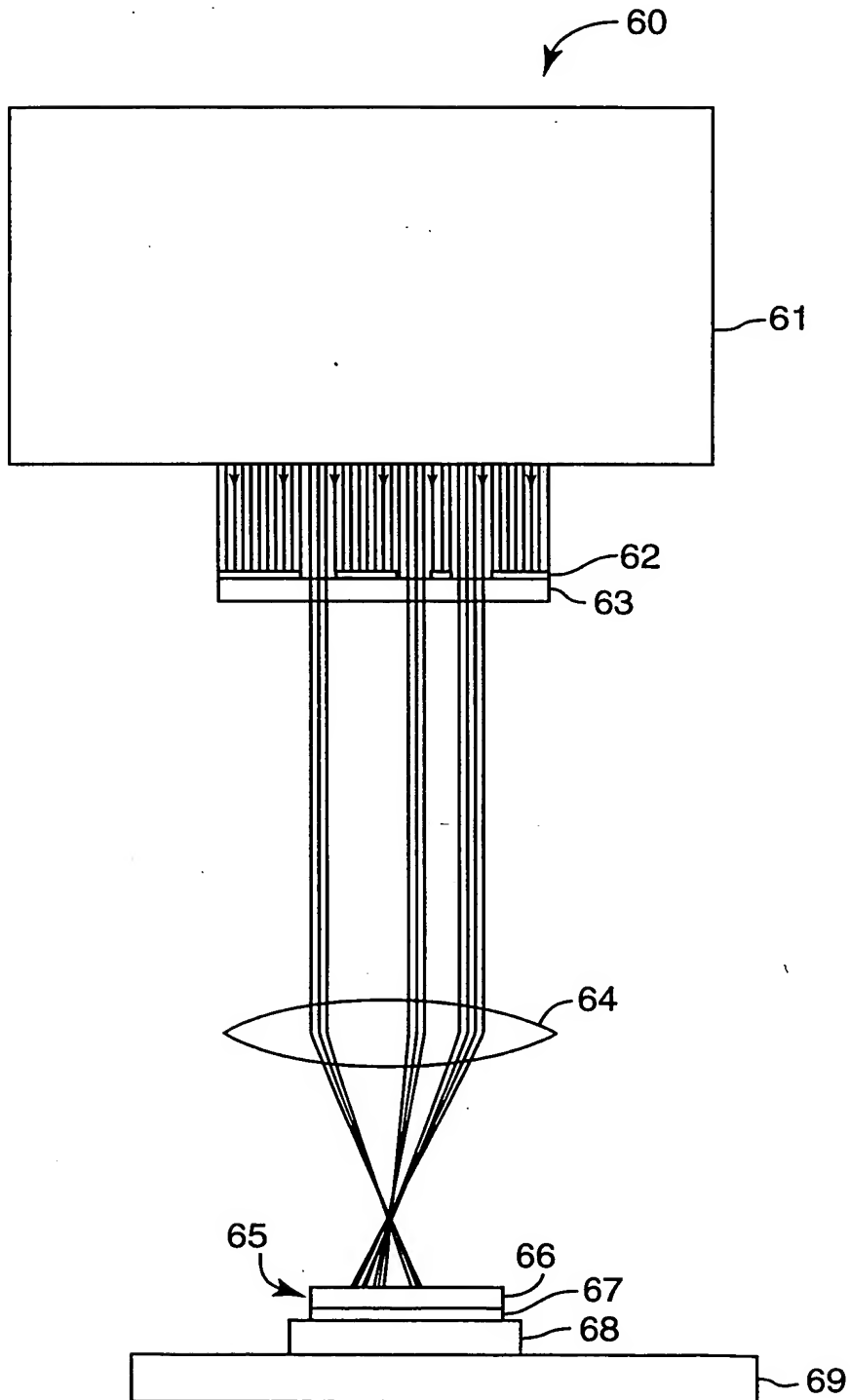


FIG. 6

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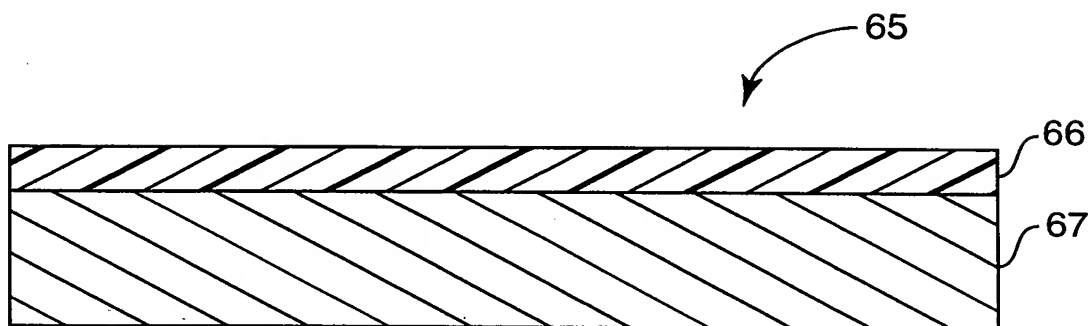


FIG. 7

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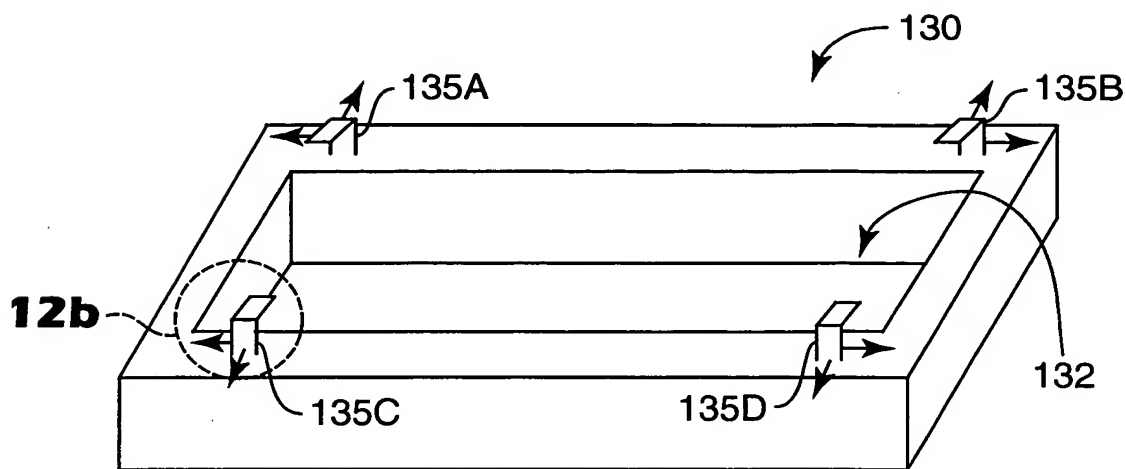


FIG. 12a

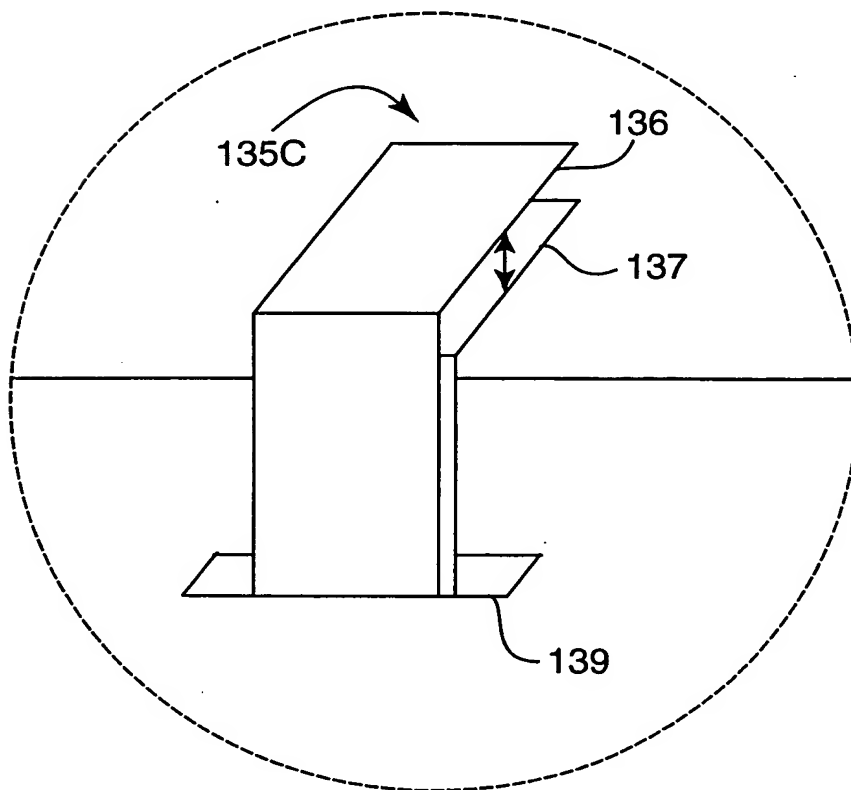


FIG. 12b

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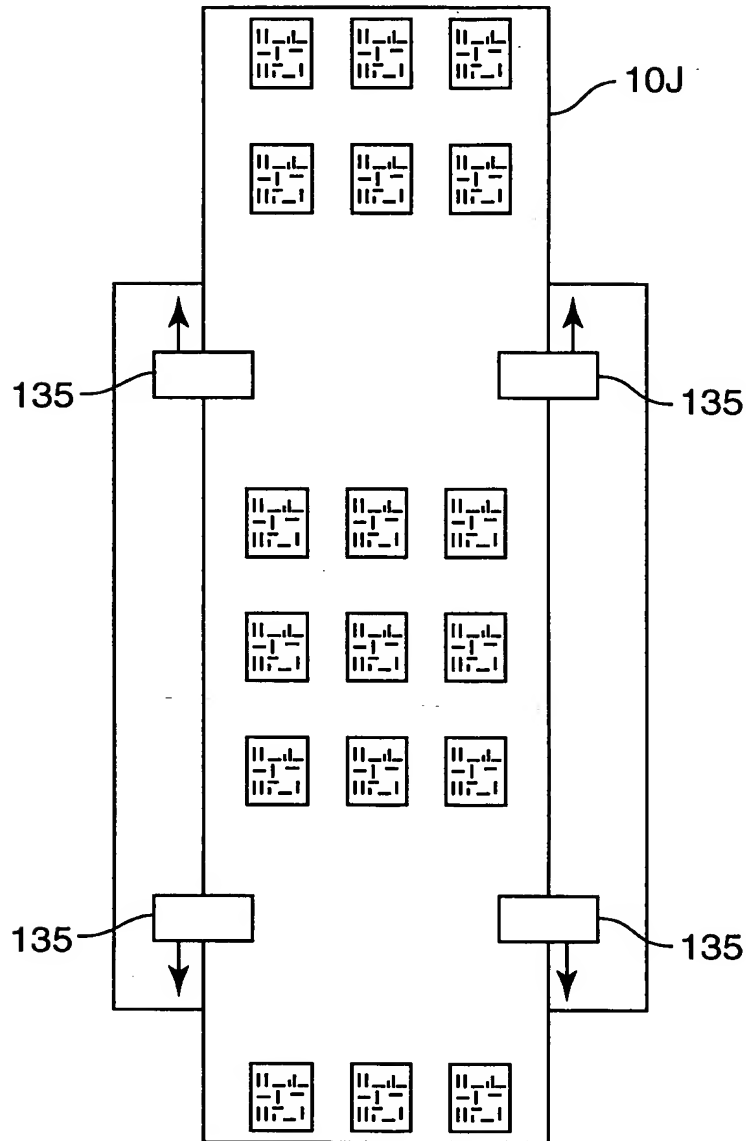


FIG. 13

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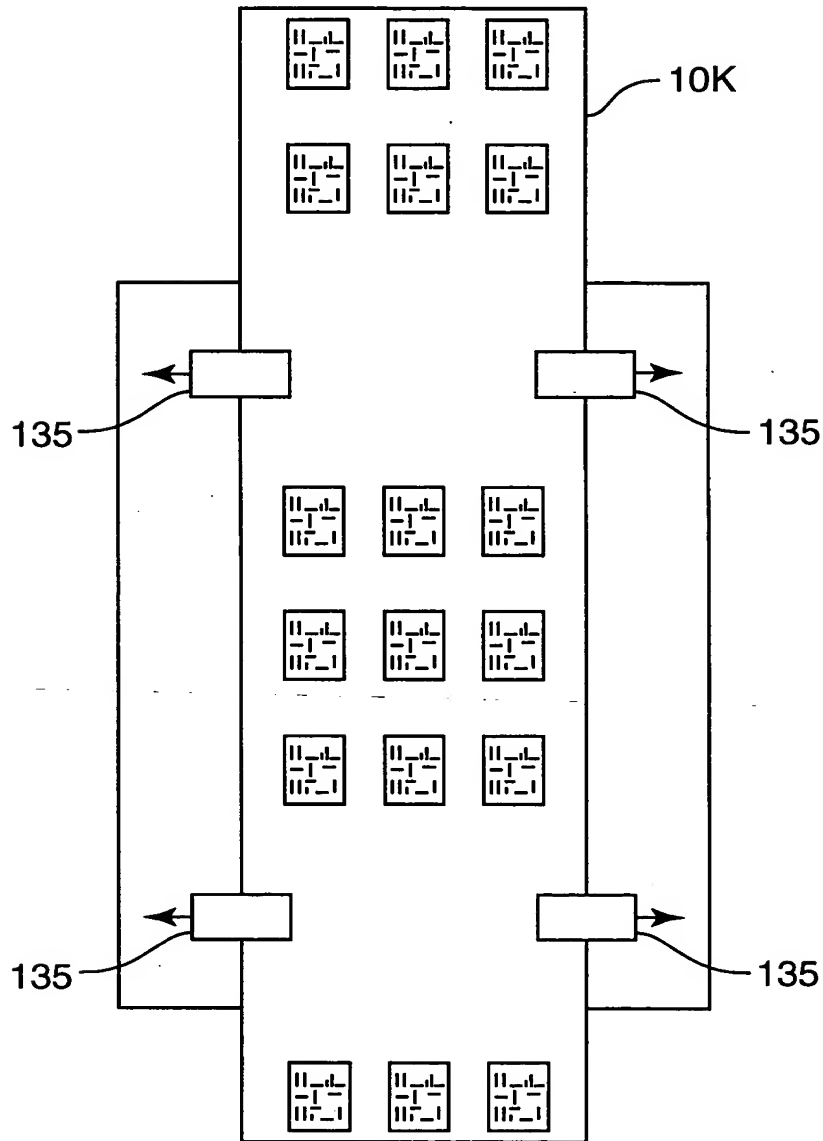


FIG. 14

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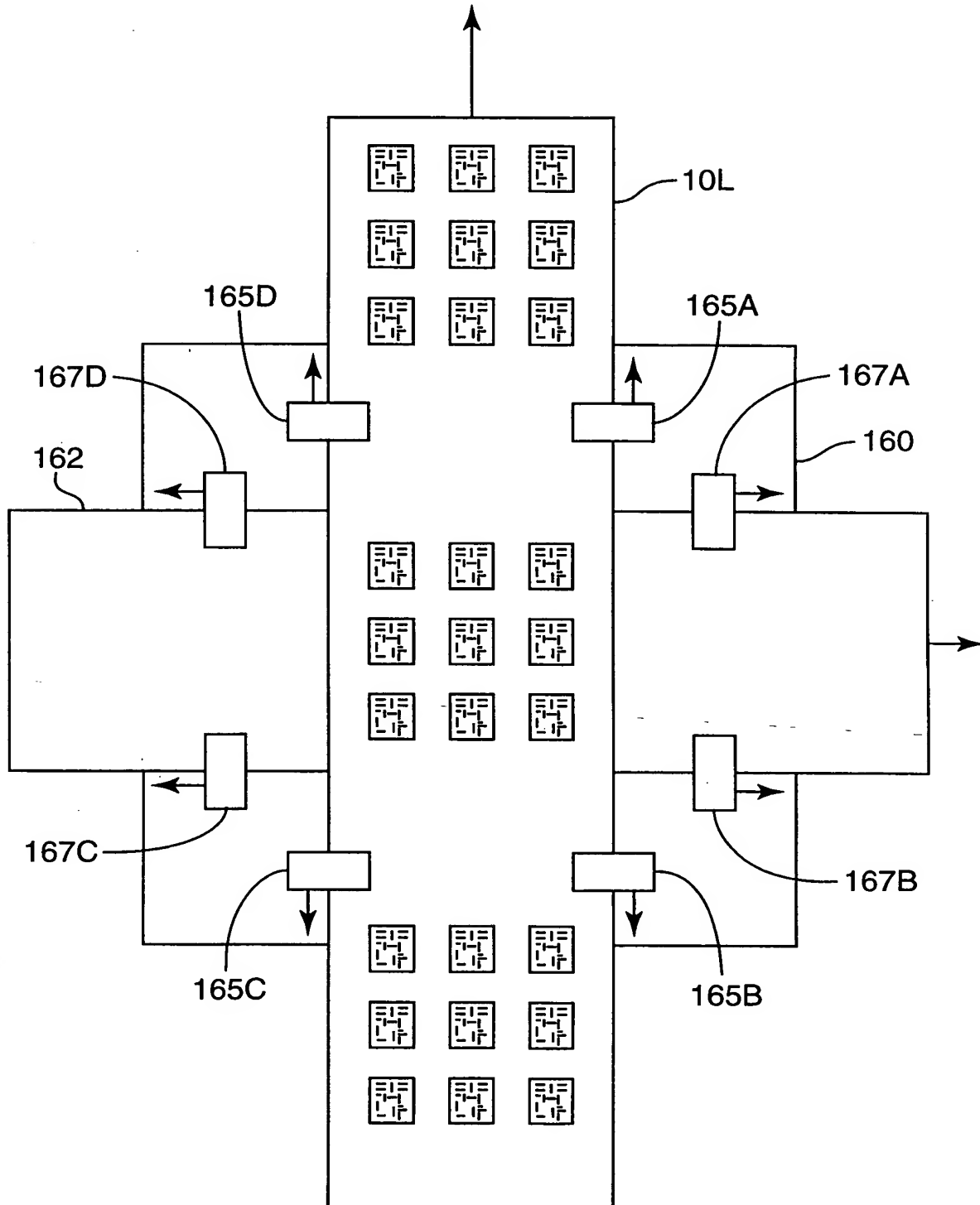


FIG. 15

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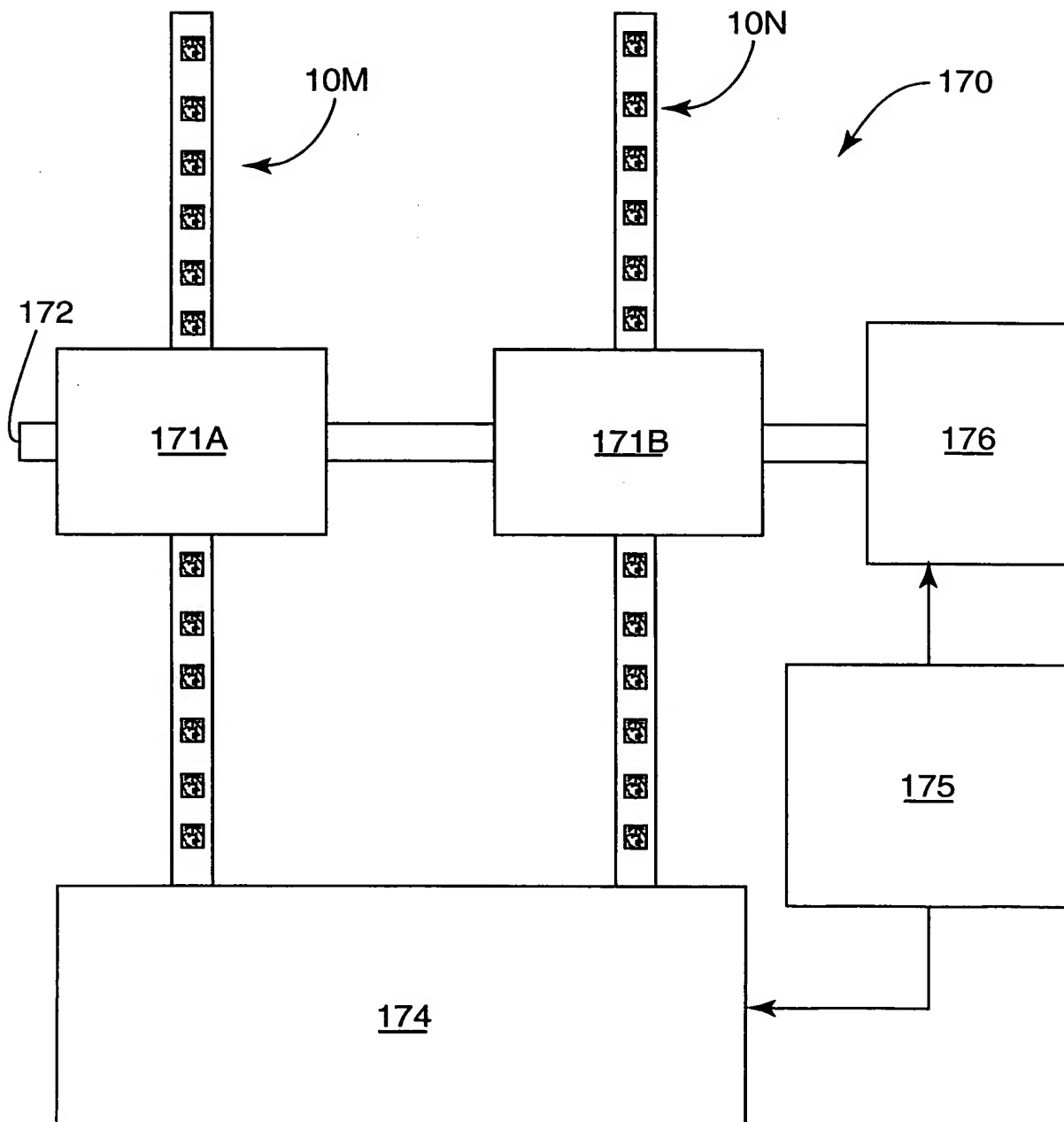


FIG. 16

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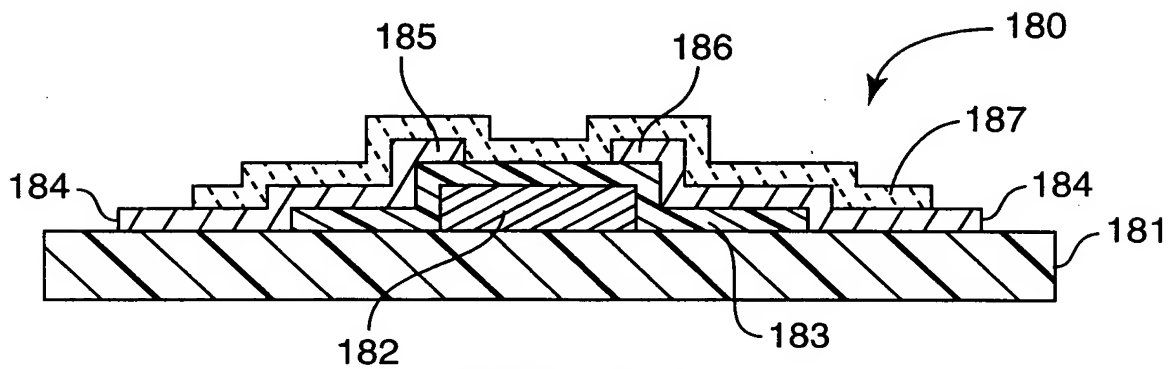


FIG. 17

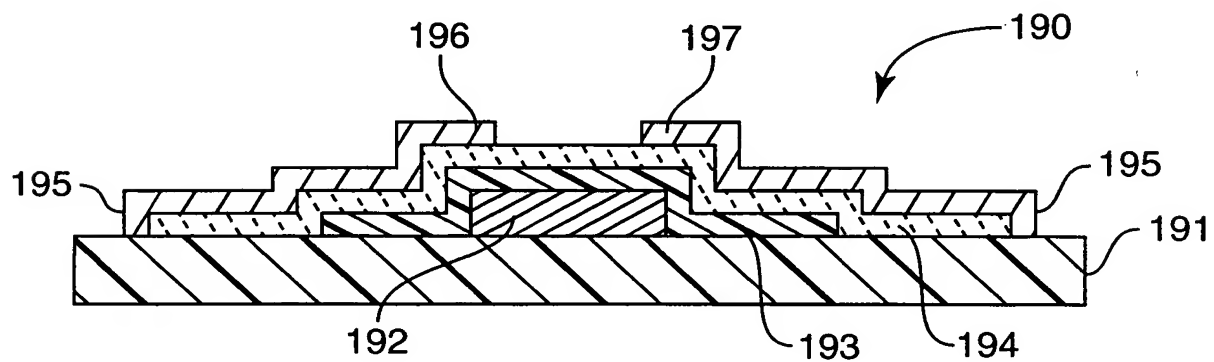


FIG. 18

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